

We claim:

1. A circuit configuration for demodulating a modulated voltage having an amplitude alternating between a low level and a high level, the circuit configuration comprising:

a high-frequency input;

a rectifier circuit connected downstream of said high-frequency input, said rectifier circuit having an output, said rectifier circuit for obtaining an input voltage;

a first charging circuit for producing a charging voltage;

a second charging circuit for producing a charging voltage, said first charging circuit and said second charging circuit connected in parallel to said output of said rectifier circuit;

a decoupling device for decoupling the charging voltage of said first charging circuit and the charging voltage of said second charging circuit when there is a prescribed ratio between the charging voltage of said first charging circuit or the charging voltage of said second charging circuit and the input voltage for said rectifier circuit; and

an evaluation circuit for ascertaining a modulation level from

the ratio of the charging voltages.

2. The circuit configuration according to claim 1, further comprising a floating current-mirror circuit for said first charging circuit and said second charging circuit.

3. The circuit configuration according to claim 1, further comprising a voltage transformer for changing the charging voltage of said first charging circuit and/or the charging voltage of said second charging circuit.

4. The circuit configuration according to claim 1, further comprising a diode for coupling said first charging circuit and said second charging circuit when there is a predetermined ratio between the charging voltage of said first charging circuit and the charging voltage of said second charging circuit.

5. The circuit configuration according to claim 1, wherein a voltage on said second charging circuit is converted into two different voltages.

6. The circuit configuration according to claim 1, wherein said first charging circuit and said second charging circuit have different discharge times.

7. The circuit configuration according to claim 1, further comprising:

a charging-current amplification circuit; and

a changeover apparatus for turning on said charging-current amplification circuit from a prescribed degree of modulation onwards.